

ANNAI J.K.K.SAMPOORANIAMMAL POLYTECHNIC COLLEGE

T.N.PALAYAM, GOBI(TK), ERODE(DT) - 638506

STAFF PROFILE

1. PERSONAL DETAILS

NAME : R.RAMESH

FATHER'S NAME : A.RAJU

2. EDUCATIONAL QUALIFICATIONS:

- **M.E – power electronics and drives** from AMS college of engineering, NAMAKKAL, Anna University, 2011
- **B.E – Electrical And Electronics Engineering** from MAHARAJA ENGINEERING COLLEGE, AVINASHI, BHARATHIAR University, 2002 with
- **Diploma in Electronics And Communication Engineering** from AJKK Sampooran iammal Polytechnic College, T.N Palayam

3.Experience

Designation: HEAD OF THE DEPARTMENT

Period: Dec'2015 to till date

Division: AJKK SAPT T.N pallyam, Gobi.

Designation: ASSISTANT PROFESSOR

Period: Dec'2010 to May 2015

Division: VIVEKANANDA COLLEGE OF TECHNOLOGY FOR WOMEN

Designation: ASSISTANT PROFESSOR

Period: Jul 2009 to Dec'2010 (18 months)

Division: Jkk munirajah college of technology T.N pallyam,

Designation: SENIOR LECTURER

Period: AUG' 2002 to APR'2007

Division: AJKK SAPT T.N pallyam, Gobi.

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3. TECHNICAL SKILLS: Matlab & Simulink-Math Work

4. PROJECT :

PROJECT NAME: modeling and controller design of the z-source inverter with inductive load

Parasitic Inductance Effect on Switching Losses for a High Frequency Dc-Dc Converter

PROJECT DESCRIPTION: It is an Invention project, in which the device is acting both Inverter and control the voltage capacity.

5. AREA OF INTEREST:

Power Electronics

Electrical Machines

Electromagnetic Theory

6. Short-term Course

Seminars & WORKSHOPS

1. Attended Two day AICTE Sponsored NATIONAL LEVEL SEMINAR ON ADVANCED CONTROL TECHNIQUES FOR ELECTRIC DRIVES Organized by Department of Electrical and Electronics Engineering, Vivekanandha Institute Of Engineering And Technology For Women , Tiruchengodu, India., 22&23 NOVEMBER, 2013.
2. Attended Two day DRDO Sponsored NATIONAL LEVEL SEMINAR ON RECENT ADVANCED IN POWER ELECTRONICS TECHNOLOGY FOR ELECTRICAL MACHINE DRIVES Organized by Department of Electrical and Electronics Engineering, Vivekanandha Institute Of Engineering And Technology For Women , Tiruchengodu, India., 29&30 JULY, 2013.
3. Attended One Week IET Sponsored WORKSHOP ON Circuit Designing Organized by Department of Electrical and Electronics Engineering, Vivekanandha college of Technology For Women, Tiruchengodu, India., 18to19 August, 2013.

7. EXTRA & CO-CURRICULAR ACTIVITY:

Exam Management System (EMS Co – ordinator) in Engineering College.

Staff co-ordinator of IEI and conducted so many programs.

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zonal level player in shuttle

8. PRESENTATIONS: paper on Analysis Of Modeling And Controller Design Of The Z-Source Inverter With Inductive Load

8. OTHER EXPERIENCES: SENIOR ENGINEER ON CANEL POWER SYSTEMS Period: MAY2015 to DEC 2015

